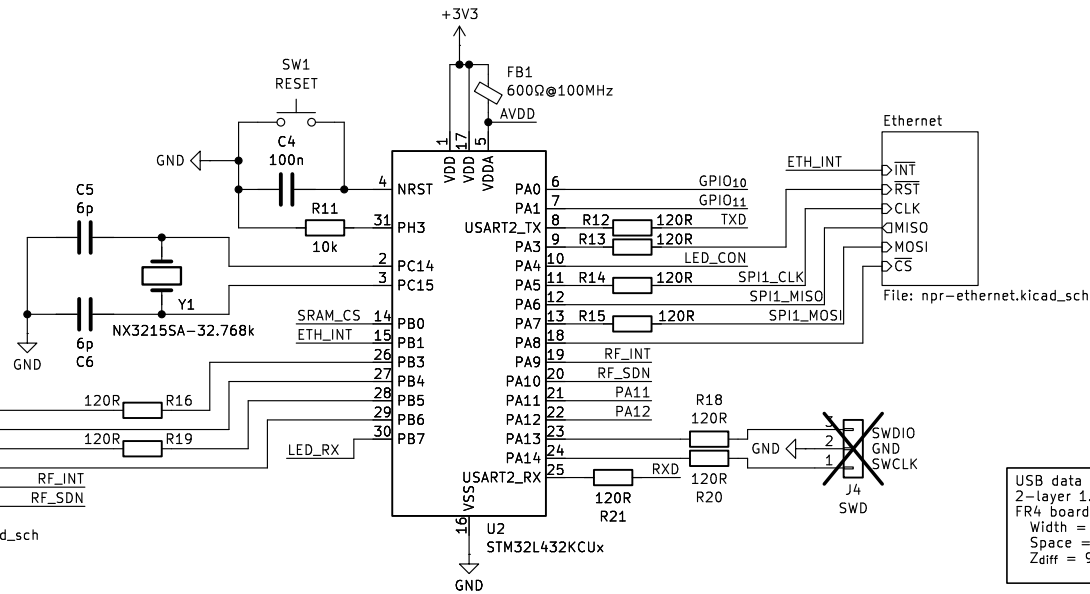
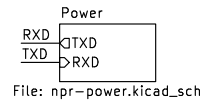
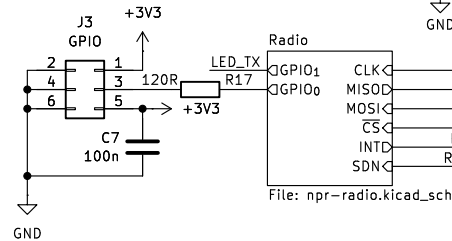
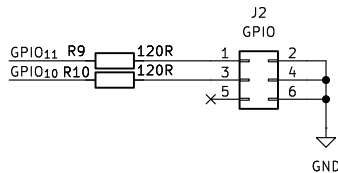
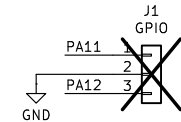
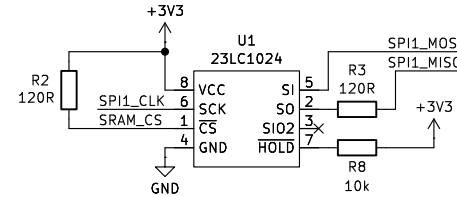
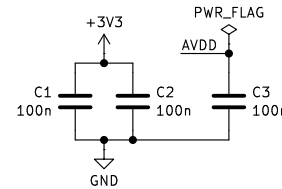
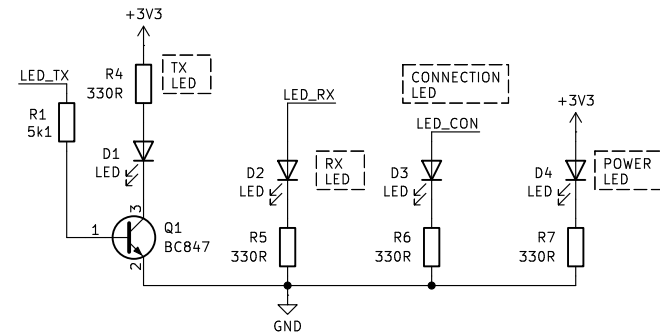
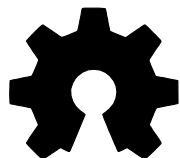




PIN	FUNCTION
PA0	GPIO10
PA1	GPIO11
PA2	UART TX
PA3	ETH Reset
PA4	LED Connection
PA5	SPI1 CLK (ETH+SRAM)
PA6	SPI1 MISO (ETH+SRAM)
PA7	SPI1 MOSI (ETH+SRAM)
PA8	ETH CS
PA9	Radio Interrupt
PA10	Radio SDN
PA11	PA11 / USB D-
PA12	PA12 / USB D+
PA13	SWDIO
PA14	SWCLK
PA15	UART RX
PB0	SRAM CS
PB1	ETH Interrupt
PB3	Radio CLK
PB4	Radio MISO
PB5	Radio MOSI
PB6	Radio CS
PB7	LED Receive

USB data lines on a 2-layer 1.6 mm thick FR4 board:
Width = 0.8 mm
Space = 0.16 mm
Zdiff = 90.18 Ω



This design is Open-Source Hardware.
Based on the project by F4HDK.
Current version by OK5VAS.

